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[Lambda set **selection** in Roth-Karp decomposition for LUT-based FPGA technology mapping - group of 7 »](#)

WZ Shen, JD Huang, SM Chao - Proceedings of the 32nd ACM/IEEE conference on Design ..., 1995 - portal.acm.org
 ... 0 in either O' or O" will make two corresponding 1 minterms to be **incompatible**. ...

Based on this idea, we propose a novel **heuristics** for 1 set **selection** in the ...

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[How considering incompatible state mergings may reduce the DFA induction search tree - group of 6 »](#)

F Coste, J Nicolas - Fourth International Colloquium on Grammatical Inference ... - Springer

... entropy measure(section 3.2) and pruning with an **incompatibility** clique (section 3.3). 3.2 Ordering Pairs of States, EntropyBased **Selection Heuristics** of every ...

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[Declarative agent control - group of 4 »](#)

A Kakas, P Mancarella, F Sadri, K Stathis, F Toni - 5th Workshop on Computational Logic in Multi-Agent Systems (... - Springer

... enabling and behaviour conditions, and in particular for **selection** functions (including the **heuristic** ones, if needed). – An **incompatibility** part, including ...

[Cited by 6 - Related Articles - Web Search - BL Direct](#)

[Investigating cue **selection** and placement in tutorial discourse - group of 12 »](#)

M Moser, JD Moore - Proceedings of the 33rd conference on Association for ..., 1995 - portal.acm.org

... approach synthesizes ideas which were previously thought **incompatible** from two ...

this has led to the development of **heuristics** for cue **selection** that take ...

Cited by 45 - Related Articles - Web Search - BL Direct

Scheduling algorithms for heterogeneous batch processors with incompatible job-families

M Mathirajan, AI Sivakumar, V Chandru - Journal of Intelligent Manufacturing, 2004 - Springer

... heterogeneous BPs in the presence of known future job arrivals with **incompatible**

job-families ... The **heuristics** for batch processor **selection** (BPS) are ...

Cited by 4 - Related Articles - Web Search - BL Direct

Dynamic Capacity Expansion Problem with Multiple Products: Technology **Selection** and Timing of ... - group of 2 »

S Li, D Tirupati - Operations Research, 1994 - JSTOR

... a critical parameter in technology **selection** and addressed ... that there is no inherent

incompatibility between scale ... that the quality of the **heuristics** may not ...

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Experiments on the automated **selection** of patients for clinical trials - group of 9 »

E Fink, LO Hall, DB Goldgof, BD Goswami, M ... -

Systems, Man and Cybernetics, 2003. IEEE International ..., 2003 - ieeexplore.ieee.org

... trial because of participation in another **incompatible** trial; the ... 2. The results

confirm that the **heuristics** reduce the cost of the **selection** process. ...

[Cited by 2](#) - [Related Articles](#) - [Web Search](#) - [BL Direct](#)

[Local search **heuristics** for the assembly line balancing problem with incompatibilities between tasks - group of 4 »](#)
J Bautista, R Suarez, M Mateo, R Companys - Robotics and Automation, 2000. Proceedings. ICRA'00. IEEE ..., 2000. -
ieeexplore.ieee.org

... but revised by introducing a **selection** probability for ... to generate solutions in the

heuristic space. ... and the Rachamadugu ratio of **incompatibility**, the values ...

[Cited by 8](#) - [Related Articles](#) - [Web Search](#) - [BL Direct](#)

[Evolution of **Incompatibility**-Inducing Microbes and Their Hosts - group of 3 »](#)

M Turelli - Evolution, 1994 - JSTOR

... Finally, the fourth term implies **selection** against incompat ... the accuracy of the

heuristic condition (12 ... the evolution of the **incompatibility** parameters versus ...

[Cited by 108](#) - [Related Articles](#) - [Web Search](#) - [BL Direct](#)

[Stochastic scheduling of a batch processing machine with incompatible job families - group of 3 »](#)

I Duenyas, JJ Neale - Annals of Operations Research, 1997 - Springer

... of a batch processing machine with **incompatible** job families. We derive new structural results for the optimal policy and develop a new **heuristic** for the cases ...

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T Davenport

A Shapiro

J Lakey

E Ryan

G Korbitt

Accelerating Adaptive Processes: **Product** Innovation in the Global **Computer** Industry. - group of 2 »

K Eisenhardt, BN Tabrizi - Administrative Science

Quarterly, 1995 - questia.com

... speed of **product** development by **maintaining** a disciplining ... the development of a new

product within an existing platform family and **computer** architecture, such ...

Cited by 441 - Related Articles - Web Search - BL Direct

Can a CPA Sell **Computer** Products and Still Give Unbiased Advice? A Growing Number of Consultants ... - group of 2 »

JA Wixson - Journal of Accountancy, 1991 - questia.com

... varied and sometimes seemingly **incompatible products** to work ... to become so knowledgeable

about **computer** and software ... firms are continuing to **maintain** the levels ...

Web Search

Putting the enterprise into the enterprise system - group of 4 »

TH Davenport - Harvard Business Review, 1998 - wits.ac.za

... of the ES implementation at a **computer** company expresses ... cals business, where many

products are com ... for transferring informa- tion across **incompatible** unit and ...

Cited by 775 - Related Articles - Web Search - BL Direct

[BOOK] **Product** Standards for Internationally Integrated Goods Markets - group of 3 »

AO Sykes - 1995 - books.google.com

... 3. Some Economics of **Product** Standardization, Regulation, and Conformity Assessment
27 ... 1960s, the Tokyo Round in the 1970s, and **most recently** the protracted ...

Cited by 70 - Related Articles - Web Search - Library Search

... : The Use of Exclusionary Pricing and Technical Incompatibility to Maintain Monopoly Power in ... - group of 2 »

KC Baseman, FR Warren-Boulton, GA Woroch - Antitrust Bulletin, 1995 - elsa.berkeley.edu

... Several key features distinguish this **product**. ... may have to replace outdated or

incompatible applications. ... For a while, Compaq **Computer** had shipped its machines ...

Cited by 11 - Related Articles - View as HTML - Web Search - BL Direct

Computer-aided design of analog and mixed-signal integratedcircuits - group of 2 »

GGE Gielen, RA Rutenbar - Proceedings of the IEEE, 2000 - ieeexplore.ieee.org

... project management goals such as final **product** cost and ... GIELEN AND RUTENBAR:

COMPUTER-AIDED DESIGN OF ANALOG ... the hierarchy in order to **maintain** consistency in ...

Cited by 124 - Related Articles - Web Search - BL Direct

Evolutionary genetics of self-**incompatibility** in the Solanaceae - group of 4 »

AD Richman, JR Kohn - Plant Molecular Biology, 2000 - Springer

... PCR **products** were cloned to separate the two ... simulation of populations under gametophytic self-**incompatibility**. ... was examined using the **computer** package LINTRE ...

[Cited by 27](#) - [Related Articles](#) - [Web Search](#) - [BL Direct](#)

Computer-Aided Software Engineering in a distributed workstation environment

DB Leblang, RP Chase Jr - Proceedings of the first ACM SIGSOFT/SIGPLAN software ..., 1984 - portal.acm.org

... is built on the Xerox PARC **Computer Science Laboratory** ... A DSEE **product** goal requires that it work with any ... whole, plain text, copy of the **most recent** version is ...

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Islet Transplantation in Seven Patients with Type 1 Diabetes Mellitus Using a Glucocorticoid-Free ... - group of 6 »

AMJ Shapiro, JRT Lakey, EA Ryan, GS Korbitt, E ... - 2000 - content.nejm.org

... 13 The use of xenoprotein **products** (such as ... assessment (HOMA) evaluation uses the **computer** program. ... **Incompatibility** between human blood and isolated islets of ...

[Cited by 1154](#) - [Related Articles](#) - [Web Search](#) - [BL Direct](#)

The effect of rapid IT change on the demand for training

JS Bernamati, AL Lederer - Proceedings of the 2001 ACM SIGCPR conference on **Computer** ..., 2001 - portal.acm.org

... Primary Business Activity Frequency Manufacturing 50 Government 31 Education 25

Computer Services 24 ... Incompatibility in those **products** challenges these ...

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Ra

1 [Fast detection of communication patterns in distributed executions](#)

Thomas Kunz, Michiel F. H. Seuren

November 1997 **Proceedings of the 1997 conference of the Centre for A
on Collaborative research**

Publisher: IBM Press

Full text available: [pdf\(4.21 MB\)](#)

Additional Information: [full citation](#), [abst](#)
[index terms](#)

Understanding distributed applications is a tedious and difficult task. Vis
on process-time diagrams are often used to obtain a better understanding
the application. The visualization tool we use is Poet, an event tracer dev
University of Waterloo. However, these diagrams are often very comple
the user with the desired overview of the application. In our experience,
repeated occurrences of non-trivial commun ...

2 [Query evaluation techniques for large databases](#)

Goetz Graefe

June 1993 **ACM Computing Surveys (CSUR)**, Volume 25 Issue 2

Publisher: ACM Press

Full text available: [pdf\(9.37](#)

Additional Information: [full citation](#), [abst](#)

MB)

citings, index ter


Database management systems will continue to manage large data volumes. Algorithms for accessing and manipulating large sets and sequences will provide acceptable performance. The advent of object-oriented and external database systems will not solve this problem. On the contrary, modern data models solve this problem: In order to manipulate large sets of complex objects as efficiently as database systems manipulate simple records, query-processing ...

Keywords: complex query evaluation plans, dynamic query evaluation plans, database systems, iterators, object-oriented database systems, operator notation, parallelization, parallel algorithms, relational database systems, set-matching, sort-hash duality

3 A structural view of the Cedar programming environment

◆ Daniel C. Swinehart, Polle T. Zellweger, Richard J. Beach, Robert B. Hagmann
August 1986 **ACM Transactions on Programming Languages and Systems**
Volume 8 Issue 4

Publisher: ACM Press


Full text available:  [pdf\(6.32 MB\)](#) Additional Information: [full citation](#), [abstracts](#), [citings](#), [index terms](#)

This paper presents an overview of the Cedar programming environment—its overall structure—that is, the major components of Cedar and the way that Cedar supports the development of programs written in a single programming language called Cedar. Its primary purpose is to increase the productivity of programming activities. Activities include experimental programming and the development of programming systems for a high-performance personal computer. T ...

4 Types and persistence in database programming languages

◆ Malcolm P. Atkinson, O. Peter Buneman
June 1987 **ACM Computing Surveys (CSUR)**, Volume 19 Issue 2

Publisher: ACM Press

Full text available:  [pdf\(7.91 MB\)](#) Additional Information: [full citation](#), [abstracts](#), [citings](#), [index terms](#)

Traditionally, the interface between a programming language and a database is through a set of relatively low-level subroutine calls, or it has required special


embedding of one language in another. Recently, the necessity of integrating programming language techniques has received some long-overdue recognition. A number of attempts have been made to construct programming languages integrated database management systems. These languages ...

5 Special issue: AI in engineering

◆ D. Sriram, R. Joobbani

April 1985 **ACM SIGART Bulletin**, Issue 92

Publisher: ACM Press

Full text available:  [pdf\(8.79 MB\)](#)

Additional Information: [full citation](#), [abstract](#)


The papers in this special issue were compiled from responses to the announcement in the July 1984 issue of the SIGART newsletter and notices posted over the ACM. The interest being shown in this area is reflected in the sixty papers received from various countries. About half the papers were received over the computer network.

6 A temporally oriented data model

◆ Gad Ariav

December 1986 **ACM Transactions on Database Systems (TODS)**, Volume 11, Number 4

Publisher: ACM Press

Full text available:  [pdf\(2.52 MB\)](#)

Additional Information: [full citation](#), [abstract](#), [citations](#), [index terms](#)


The research into time and data models has so far focused on the identification of a model to the classical relational model that would provide it with "adequate" semantics to deal with time. The temporally oriented data model (TODM) presented in this paper is the result of a different approach, namely, it directly operationalizes the pervasive metaphor for time. One of the main results is thus the development of the data cube: a three-dimensional data model.

7 The Pan language-based editing system

◆ Robert A. Ballance, Susan L. Graham, Michael L. Van De Vanter

January 1992 **ACM Transactions on Software Engineering and Methodology**, Volume 1, Issue 1

Publisher: ACM Press

Full text available:  [pdf\(2.43 MB\)](#)


Additional Information: [full citation](#), [abstract](#), [citations](#), [index terms](#)

Powerful editing systems for developing complex software documents an engineer. Besides requiring efficient incremental algorithms and comple such editors must accommodate flexible editing styles, provide a consist powerful user interface, support individual variations and projectwide co maintain a sharable database of information concerning the documents b integrate smoothly with the other tools in the environme ...

Keywords: Ladle, Pan, coherent user interfaces, colander, contextual co facilities, grammatical abstraction, interactive programming environmen programming, logical constraint grammar, reason maintenance, syntax-r tolerance for errors and anomalies

8 Markup systems and the future of scholarly text processing


◆ James H. Coombs, Allen H. Renear, Steven J. DeRose
November 1987 **Communications of the ACM**, Volume 30 Issue 11
Publisher: ACM Press

Full text available:  pdf(1.91 MB) Additional Information: [full citation](#), [abst citings](#), [index ter](#)

Markup practices can affect the move toward systems that support schol thinking and writing. Whereas procedural and presentational markup sys movement, descriptive markup systems accelerate the pace by simplifyir and allowing the authors to focus their attention on the content.

9 Object orientation in multidatabase systems

◆ Evaggelia Pitoura, Omran Bukhres, Ahmed Elmagarmid
June 1995 **ACM Computing Surveys (CSUR)**, Volume 27 Issue 2
Publisher: ACM Press

Full text available:  pdf(4.85 MB) Additional Information: [full citation](#), [abst citings](#), [index ter](#)

A multidatabase system (MDBS) is a confederation of preexisting distrib heterogeneous, and autonomous database systems. There has been a rece research suggesting the application of object-oriented techniques to facil task of designing and implementing MDBSs. Although this approach see lack of a general framework impedes any further development. The goal provide a concrete analysis and categorization of the various ...


Keywords: distributed objects, federated databases, integration, multida

10 Compiling nested data-parallel programs for shared-memory multiprocessors

◆ Siddhartha Chatterjee

July 1993 **ACM Transactions on Programming Languages and Systems**
Volume 15 Issue 3

Publisher: ACM Press

Full text available:  pdf(4.17 MB) Additional Information: [full citation](#), [reference](#), [index terms](#), [review](#)


Keywords: compilers, data parallelism, shared-memory multiprocessors

11 Trace-driven memory simulation: a survey

◆ Richard A. Uhlig, Trevor N. Mudge

June 1997 **ACM Computing Surveys (CSUR)**, Volume 29 Issue 2

Publisher: ACM Press

Full text available:  pdf(636.11 KB) Additional Information: [full citation](#), [abstract](#), [citations](#), [index terms](#)

As the gap between processor and memory speeds continues to widen, re-evaluating memory system designs before they are implemented in hardware is increasingly important. One such method, trace-driven memory simulation, is a subject of intense interest among researchers and has, as a result, enjoyed significant and substantial improvements during the past decade. This article surveys recent developments by establishing criteria for evaluating trace-driven simulation.


Keywords: TLBs, caches, memory management, memory simulation, trace-driven simulation

12 Multidimensional access methods

◆ Volker Gaede, Oliver Günther

June 1998 **ACM Computing Surveys (CSUR)**, Volume 30 Issue 2


Publisher: ACM Press

Full text available:  pdf(1.05 MB) Additional Information: [full citation](#), [abstract](#), [citations](#), [index terms](#)


Search operations in databases require special support at the physical level. In conventional databases as well as spatial databases, where typical search is the point query (find all objects that contain a given search point) and the range query (find all objects that overlap a given search region). More than ten years of spatial database research have resulted in a great variety of multidimensional access methods.

Keywords: data structures, multidimensional access methods

13 [The model-assisted global query system for multiple databases in distributed environments](#)


 Waiman Cheung, Cheng Hsu

October 1996 **ACM Transactions on Information Systems (TOIS)**, Volume 14, Number 4
Publisher: ACM Press


Full text available:  pdf(697.73 KB) Additional Information: [full citation](#), [abstract](#), [citations](#), [index terms](#)

Today's enterprises typically employ multiple information systems, which are independently developed, locally administered, and different in logical or physical design. A fundamental challenge in enterprise information management is the sharing of information for enterprise users across organizational boundaries; this requires a global information system capable of providing on-line intelligent assistance to users. Conventional systems such as schema-based query languages and hierarchical databases have not been able to meet this challenge.

14 [Concurrency in linear hashing](#)

 Carla Schlatter Ellis

June 1987 **ACM Transactions on Database Systems (TODS)**, Volume 12, Number 2
Publisher: ACM Press

Full text available:  pdf(1.88 MB) Additional Information: [full citation](#), [abstract](#), [citations](#), [index terms](#)


Concurrent access to complex shared data structures, particularly structures used in database indices, has long been of interest in the database community. In tree structures such as B-trees have been used as indices because of their efficient growth; whereas hashing has been used for fast access in relatively static structures. Recently, a number of techniques for dynamic hashing have appeared. The major deficiency of traditional hashing when applied to dynamic structures is that it requires a large amount of space for the bucket pointers.

15 A lifecycle process for the effective reuse of commercial off-the-shelf (CO

◆ Christine L. Braun

May 1999 **Proceedings of the 1999 symposium on Software reusability**

Publisher: ACM Press


Full text available:  pdf(949.66 KB) Additional Information: [full citation](#), [refe](#)

Keywords: COTS, architectures, life-cycle process, product lines, reuse

16 Workshop on compositional software architectures: workshop report

◆ May 1998 **ACM SIGSOFT Software Engineering Notes**, Volume 23 Iss

Publisher: ACM Press


Full text available:  pdf(2.91 MB) Additional Information: [full citation](#), [inde](#)

17 Invited paper: Future directions of machine translation

Jun-ichi Tsujii

August 1986 **Proceedings of the 11th coference on Computational lingu**

Publisher: Association for Computational Linguistics



Full text available:  pdf(1.46 MB) Additional Information: [full citation](#), [refe](#)

18 PHRED: a generator for natural language interfaces

Paul S. Jacobs


October 1985 **Computational Linguistics**, Volume 11 Issue 4

Publisher: MIT Press

Full text available:  pdf(2.16 MB)  Additional Information: [full citation](#), [abst](#)
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PHRED (PHRasal English Diction is a natural language generator design variety of domains. It was constructed to share a knowledge base with P ANalyzer) as part of a real-time user-friendly interface. The knowledge l *pattern-concept pairs*, i.e., associations between linguistic structures and templates. Using this knowledge base, PHRED produces appropriate and natural language output from a conceptual representation.PHRED and ...

19 Session 5: Undoing any operation in collaborative graphics editing systems

 David Chen, Chengzheng Sun

September 2001 **Proceedings of the 2001 International ACM SIGGROI
Supporting Group Work**


Publisher: ACM Press

Full text available:  [pdf\(249.64 KB\)](#) Additional Information: [full citation](#), [abst](#)
[citations](#), [index ter](#)

Undo is a useful and widely supported feature which can be used to reco operations, learn new system features, and explore alternative solutions. any operation at any time is especially important for collaborative editing; can be used to support local or global undo and also multiple undo mode solution presented in this paper is able to undo any operation in collabor editing systems. The major challenge in desi ...

Keywords: collaborative editing, concurrency control, consistency main computing, graphics editing, multi-versioning, undo/redo

20 The Howitzer improvement program: lessons learned

 D. Krantz




January 1989 **Proceedings of the conference on Tri-Ada '89: Ada techn application, development, and deployment**

Publisher: ACM Press

Full text available:  [pdf\(1.59 MB\)](#) Additional Information: [full citation](#), [refe](#)

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Xi-an Zhu, Melvin A. Breuer

November 1986 **Proceedings of 1986 ACM Fall joint computer conference****Publisher:** IEEE Computer Society Press
 Full text available: [pdf\(1.08 MB\)](#) Additional Information: [full citation](#), [reference](#), [index terms](#)

2 [Lambda set selection in Roth-Karp decomposition for LUT-based FPGA technology](#)

Wen-Zen Shen, Juinn-Dar Huang, Shih-Min Chao

January 1995 **Proceedings of the 32nd ACM/IEEE conference on Design Automation****Publisher:** ACM Press
 Full text available: [pdf\(258.76 KB\)](#) Additional Information: [full citation](#), [reference](#), [index terms](#)

3 [Instruction selection for embedded DSPs with complex instructions](#)

R. Leupers, P. Marwedel

September 1996 **Proceedings of the conference on European design automation****Publisher:** IEEE Computer Society Press
 Full text available: [pdf\(290.83 KB\)](#) Additional Information: [full citation](#), [reference](#), [index terms](#)


KB)index terms

4 Minimizing communication while preserving parallelism

◆ Wayne Kelly, William Plugh

January 1996 **Proceedings of the 10th international conference on Supercomputing**

Publisher: ACM Press


Full text available:  pdf(1.11 MB) Additional Information: full citation, reference, index terms

5 Innovations in logic synthesis: A recursive paradigm to solve Boolean relations

◆ David Bañeres, Jordi Cortadella, Mike Kishinevsky

June 2004 **Proceedings of the 41st annual conference on Design automation and test in VLSI**

Publisher: ACM Press

Full text available:  pdf(129.43 KB) Additional Information: full citation, abstract, index terms

A recursive algorithm for solving Boolean relations is presented. It provides the following features: wide exploration of solutions, parametrizable cost function and experimental results show the applicability of the method and tangible improvement in regard to previous heuristic approaches.


Keywords: Boolean relations, decomposition, logic design

6 An approach to guided incremental specification

Thomas Gabler, Sabine März-Rössel

December 1995 **Proceedings of the conference on European design automation conference**

Publisher: IEEE Computer Society Press


Full text available:  pdf(582.12 KB) Additional Information: full citation, reference, index terms

7 Area and delay mapping for table-look-up based field programmable gate arrays

P. Sawkar, D. Thomas

July 1992 **Proceedings of the 29th ACM/IEEE conference on Design automation and test in VLSI**

Publisher: IEEE Computer Society Press


Full text available:  [pdf\(571.21 KB\)](#) Additional Information: [full citation](#), [reference index terms](#)

8 A heuristic chip-level test generation algorithm

Daniel S. Barclay, James R. Armstrong

July 1986 **Proceedings of the 23rd ACM/IEEE conference on Design and**

Publisher: IEEE Press

Full text available:  [pdf\(556.00 KB\)](#) Additional Information: [full citation](#), [abstracts](#), [citations](#), [index terms](#)


An algorithm is given for generating tests from chip-level functional descriptions. The algorithm uses a chip-level fault model to define faults and fault sensitization, and uses the hardware description language (HDL) definition to solve for faults. Artificial intelligence techniques of goal trees and rule databases are used in the algorithm in ProLog. The goal types and solving strategies are outlined. The ProLog implementation is discussed. ...

9 Code scheduling: Integrated prepass scheduling for a Java Just-In-Time compiler architecture

Tatsushi Inagaki, Hideaki Komatsu, Toshio Nakatani


March 2003 **Proceedings of the international symposium on Code generation and optimization: feedback-directed and runtime optimization**

Publisher: IEEE Computer Society

Full text available:  [pdf\(835.29 KB\)](#) Additional Information: [full citation](#), [abstracts](#), [citations](#), [index terms](#)


We present a new integrated prepass scheduling (IPS) algorithm for a Java Just-In-Time (JIT) compiler, which integrates register minimization into list scheduling. To reduce the overhead of backtracking in the list scheduling when we have used up all the available instructions, we incrementally maintain a set of instructions for undoing scheduling. To maximize the ILP after undoing scheduling, we select an instruction chain with the smallest increase in the total execution time. ...

10 A semisupervised learning method to merge search engine results

 Luo Si, Jamie Callan

October 2003 **ACM Transactions on Information Systems (TOIS)**, Vol 21, No 4, pp. 381-401

Publisher: ACM Press

Full text available:  pdf(463.96 KB) Additional Information: [full citation](#), [abstracts](#), [citations](#), [index terms](#)

The proliferation of searchable text databases on local area networks and the problem of finding information that may be distributed among many databases (*distributed information retrieval*). How to merge the results from many databases is an important subproblem of the distributed information retrieval research assumed that either resource providers cooperate to provide nor or search clients download all retrieved information.


Keywords: Distributed information retrieval, resource ranking, resource merging, semisupervised learning method, server selection

11 Decision making: a missing facet of effective documentation

 Michael J. Albers

October 1996 **Proceedings of the 14th annual international conference on documentation: Marshaling new technological forces: business, academic, and user-oriented triangle**

Publisher: ACM Press



Full text available:  pdf(1.05 MB) Additional Information: [full citation](#), [references](#), [index terms](#)

12 Decomposition and technology mapping of speed-independent circuits using

Jordi Cortadella, Michael Kishinevsky, Alex Kondratyev, Luciano Lavagnolo, Alex Yakovlev

November 1997 **Proceedings of the 1997 IEEE/ACM international conference on Computer-aided design**

Publisher: IEEE Computer Society

Full text available:  pdf(169.27 KB)  Additional Information: [full citation](#), [abstracts](#), [citations](#), [index terms](#)
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Presents a new technique for the decomposition and technology mapping of speed-independent circuits. An initial circuit implementation is obtained in the

complex gates, which may not be available in the design library. The procedure iteratively performs Boolean decomposition of each such gate F into a two-input combinational or sequential gate G , which is available in the library, and H , which are simpler than F , while preserving the original ...

Keywords: Boolean decomposition, Boolean relations, circuit CAD, core decomposition, logic sharing, design library, library matching, logic decomposition, resynthesis, netlist, optimization, signal insertion, speed-independent circuit mapping, two-input combinational gate, two-input sequential gate

13 Full Technical Papers: MORE for less: model recovery from visual interface application design



Yves Gaeremynck, Lawrence D. Bergman, Tessa Lau

January 2003 **Proceedings of the 8th international conference on Intelligent interfaces**

Publisher: ACM Press

Full text available: pdf(307.44 KB) Additional Information: [full citation](#), [abstracts](#), [citations](#), [index terms](#)

An emerging approach to multi-device application development requires an abstract semantic model that is translated into specific implementations: PDAs, voice systems and other user interfaces. Specifying abstract semantics for designers accustomed to working with concrete screen-oriented layout is a new approach to model recovery: inferring semantic models from existing applications. This approach allows developers to use familiar tools but still ...

Keywords: model recovery, multi-device application development, reverse engineering systems, semantic modeling

14 High-quality sub-function construction in functional decomposition based on relationship measures

L. Józwiak, A. Chojnacki


March 2001 **Proceedings of the conference on Design, automation and test in Europe**

Publisher: IEEE Press





Full text available: pdf(240.09 KB) Additional Information: [full citation](#), [references](#), [index terms](#)


15 Incremental parsing and reason maintenance

Mats Wirén


August 1990 **Proceedings of the 13th conference on Computational ling****Publisher:** Association for Computational LinguisticsFull text available:  [pdf\(679.31 KB\)](#) Additional Information: [full citation](#), [abst](#)
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The purpose of this paper is to compare different ways of adopting reason techniques in incremental parsing (and interpretation). A reason-maintenance supports incremental formation and revision of beliefs. By viewing the partial analyses of a text as analogous to forming beliefs about the meaning relation between parsing and reason maintenance can be conceived. In language maintenance can be used for realizing a strong notion of ...


16 A fast state reduction algorithm for incompletely specified finite state machines Hiroyuki Higuchi, Yusuke MatsunagaJune 1996 **Proceedings of the 33rd annual conference on Design automation****Publisher:** ACM PressFull text available:  [pdf\(82.65 KB\)](#) Additional Information: [full citation](#), [reference](#)
[index terms](#)**17 Permanent object storage and collections for object-oriented systems with persistent objects** Bogdan Czejdo, Christoph Eick, Malcolm TaylorApril 1992 **Proceedings of the 1992 ACM/SIGAPP Symposium on Applications and technological challenges of the 1990's****Publisher:** ACM PressFull text available:  [pdf\(782.47 KB\)](#) Additional Information: [full citation](#), [reference](#)**18 Selective private function evaluation with applications to private statistics** Ran Canetti, Yuval Ishai, Ravi Kumar, Michael K. Reiter, Ronitt Rubinfeld, WrightAugust 2001 **Proceedings of the twentieth annual ACM symposium on**

distributed computing**Publisher:** ACM PressFull text available:  [pdf\(987.01 KB\)](#) Additional Information: [full citation](#), [abstracts](#), [index terms](#)

Motivated by the application of private statistical analysis of large databases, the problem of *selective private function evaluation* (SPFE). In this problem, a client interacts with one or more servers holding copies of a database $x = x_1, \dots, x_m$ to compute $f(x_1, \dots, x_m)$.

19 A reduced multipipeline machine description that preserves scheduling constraints Alexandre E. Eichenberger, Edward S. DavidsonMay 1996 **ACM SIGPLAN Notices , Proceedings of the ACM SIGPLAN on Programming language design and implementation PLDI**
Issue 5**Publisher:** ACM PressFull text available:  [pdf\(1.45 MB\)](#) Additional Information: [full citation](#), [abstracts](#), [index terms](#)

High performance compilers increasingly rely on accurate modeling of target machine resources to efficiently exploit the instruction level parallelism of an application. In this paper, we propose a reduced machine description that results in faster detection of resource contentions while preserving the scheduling constraints present in the original machine description. The proposed approach reduces a machine description in an efficient and free fashion. Moreover, it fully supports ...




20 A unified approach to input-output encoding for FSM state assignment Maciej J. Ciesielski, Jia-Jye Shen, Marc DavioJune 1991 **Proceedings of the 28th conference on ACM/IEEE design automation****Publisher:** ACM PressFull text available:  [pdf\(724.44 KB\)](#) Additional Information: [full citation](#), [references](#), [index terms](#)

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